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FIG. 1
LOGIC CONES

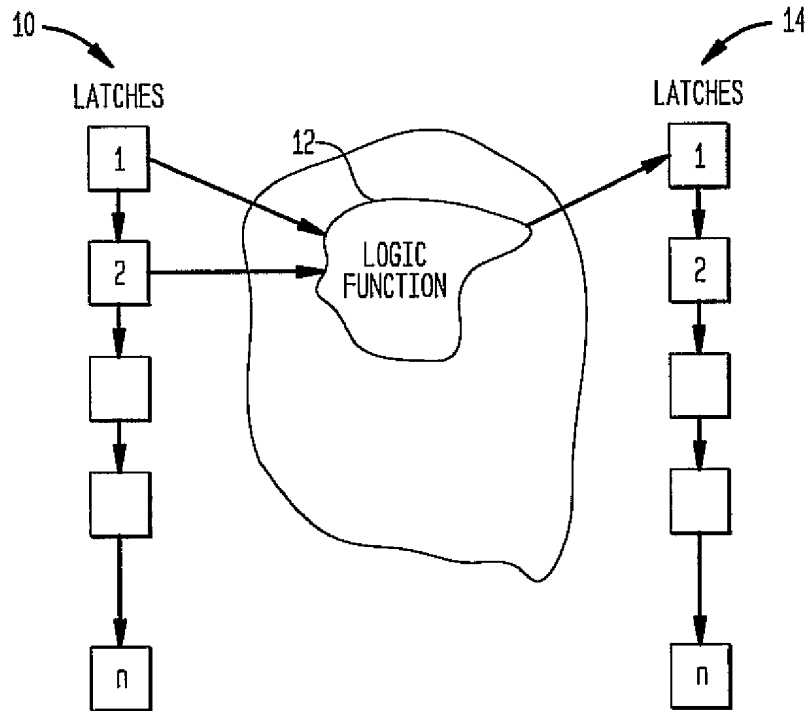
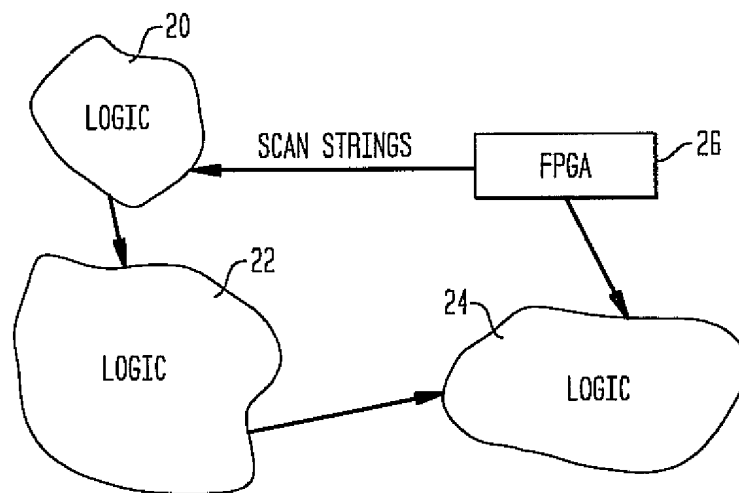


FIG. 2
STRUCTURE



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FIG. 3
FUNCTION TO FPGA

